

DSR-14446



January 22, 2004

To: Commissioner for Patents
P.O.Box 1450
Alexandria, VA 22313-1450

Fr: George O. Saile, Reg. No. 19,572
28 Davis Avenue
Poughkeepsie, N.Y. 12603

Subject: | Serial No. 10/706,839 11/12/03 |

Taner Dosluoglu

VERTICAL CHARGE TRANSFER ACTIVE
PIXEL SENSOR

INFORMATION DISCLOSURE STATEMENT

Enclosed is Form PTO-1449, Information Disclosure Citation
In An Application.

The following Patents and/or Publications are submitted to
comply with the duty of disclosure under CFR 1.97-1.99 and
37 CFR 1.56.

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being
deposited with the United States Postal Service as first class
mail in an envelope addressed to: Commissioner for Patents,
P.O. Box 1450, Alexandria, VA 22313-1450, on January 27, 2004.

Stephen B. Ackerman, Reg.# 37761

Signature/Date

S.B.Ackerman 1/27/04

U.S. Patent 6,501,109 to Chi, "Active CMOS Pixel with Exponential Output Based on the GIDL Mechanism," describes an active CMOS pixel sensor with a p well within a deep n well.

U.S. Patent 6,001,667 to Saitoh et al., "Method of Manufacturing a Semiconductor Detector for Detecting Light and Radiation," describes a method of manufacturing a semiconductor detector for detecting light radiation showing a p well within a deep n well.

U.S. Patent 5,600,127 to Kimata, "Solid State Image Sensor Transferring Charges with High Efficiency and Decreased Power Consumption, and Method of Driving the Same," describes vertical charge transfer using an n well.

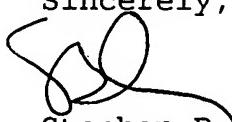
U.S. Patent 5,210,433 to Ohsawa et al., "Solid-State CCD Imaging Device with Transfer Gap Voltage Controller," describes a solid state CCD imaging device showing a potential well and a deep potential well for vertical charge transfer.

U.S. Patent 5,040,038 to Yutani et al., "Solid-State Image Sensor," describes a solid state image sensor showing a transfer potential well under a transfer electrode.

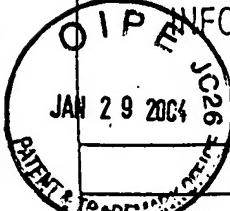
DSR-14446

U.S. Patent 4,875,101 to Endo et al., "Solid State Photovoltaic Imaging Device with Excess Charge Eliminator," describes a solid state imaging device showing shallow and deep potential wells for vertical charge transfer.

Sincerely,



Stephen B. Ackerman,
Reg. No. 37761



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**INFORMATION DISCLOSURE CITATION
IN AN APPLICATION**

JAN 29 2004

(Use several sheets if necessary)

Doctor Number (Specifying)

DSR-14446

Aggression Number

10/706,839

Appleton

Taner Dosluoglu

Filing Date

11/12/03

Group Action

U. S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

| | DOCUMENT NUMBER | DATE | COUNTRY | CLASS | SUBCLASS | TRANSLATION | |
|--|-----------------|------|---------|-------|----------|-------------|----|
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OTHER DOCUMENTS (Including Author, Title, Date, Portion/Pages, Etc.)

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EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.